

Confirmation No.9703

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|--|-----------------|---------------------|
| Applicant: | Deixler | Examiner: | Pompey, Ron Everett |
| Serial No.: | 10/587661 | Group Art Unit: | 2812 |
| Filed: | 10/30/2007 | Docket No.: | US030427US2 |
| Title: | METHOD OF FABRICATING A SiGe SEMICONDUCTOR STRUCTURE | | |

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No.
65913

Dear Sir:

This communication is in reply to the Office Action dated November 16, 2009 in which a restriction/election requirement was presented. In response thereto, Applicant provisionally elects Species 1 and Claims 1 -12 with traverse.

A complete listing of the Claims follows on page 2.

Remarks begin on page 4.